O.M. 1 CO 14-30				ATTORNEY DOCKET NO.	SERIAL NO.	
LIST OF PRIOR ART CITED			HTPB.	AUS920040064US1	10/806,871	
BY APPLICANT E			16/			
		- nin		APPLICANT Dimpsey et al.		
		\\ <u>R</u> .30"		FILING DATE March 22, 2004	GROUP ART UNIT	<del>-2105</del> 2185
		DOCUMENT	U Carrier	S. PATENT DOCUMENTS		
EXAMINE	₹	DOCUMENT		INVENTOR NAME	CLASS/	FILING
INITIAL	1	NO.	DATE	Diamoial	SUBCLASS	DATE
<i>A</i> ,>		5,103,394		Blasciak	395/575	Dec. 21, 1989
' '	AB	6,330,662 B1	Dec. 11, 2001		712/236	Feb. 23, 1999
	AC	6,480,938 B2	Nov. 12, 2002		711/125	Dec. 15, 2000
	AD	6,430,741 B1		Mattson, Jr. et al.	717/154	Feb. 26, 1999
	AE	6,189,141 B1	Feb. 13, 2001	Benitez et al.	717/4	May 4, 1998
	AF	5,930,508	Jul. 27, 1999	Faraboschi et al.	395/706	Jun. 9, 1997
	AG	6,351,844 B1	Feb. 26, 2002	Bala	717/4	Nov. 5, 1998
	AH	6,324,689 B1	Nov. 27, 2001	Lowney et al.	717/9	Sep. 30, 1998
	Al	6,442,585 B1	Aug. 27, 2002	Dean et al.	709/108	Nov. 26, 1997
	AJ	5,774,724	Jun. 30, 1998	Heisch	395/704	Nov. 20, 1995
	AK	5,987,250	Nov. 16, 1999	Subrahmanyam	395/704	Aug. 21, 1997
	AL	6,192,513 B1	Feb. 20, 2001	Subrahmanyam	717/5	Nov. 2, 1998
	AM	5,691,920	Nov. 25, 1997	Levine et al.	364/551.01	Oct. 2, 1995
	AN	6,223,338 B1	Apr. 24, 2001	Smolders	717/4	Sep. 30, 1998
	AO	6,101,524	Aug. 8, 2000	Choi et al.	709/102	Oct. 23, 1997
	AP	6,256,775 B1	Jul. 3, 2001	Flynn	717/4	Dec. 11, 1997
	AQ	6,446,029 B1	Sep. 3, 2002	Davidson et al.	702/186	Jun. 30, 1999
	AR	6,134,676	Oct. 17, 2000	VanHuben et al.	714/39	Apr. 30, 1998
	AS	5,937,437	Aug. 10, 1999	Roth et al.	711/202	Oct. 28, 1996
	AT	6,243,804 B1	Jun. 5, 2001	Cheng	712/228	Jul. 22, 1998
	AU	4,291,371	Sep. 22, 1981	Holtey	364/200	Jan. 2, 1979
1	AV	5,938,778	Aug. 17, 1999	John, Jr. et al.	714/45	Nov. 10, 1997
	AW	6,286,132 B1	Sep. 4, 2001	Tanaka et al.	717/4	Jan. 7, 1999
	AX	6,206,584 B1	Mar. 27, 2001	Hastings	395/183.11	May 31, 1995
	AY	6,374,364 B1	Apr. 16, 2002	McElroy et al.	714/10	Jan. 19, 1999
	AZ	6,070,009	May 30, 2000	Dean et al.	395/704	Nov. 26, 1997
1,5	ВА	5,966,537	Oct. 12, 1999	Ravichandran	395/709	May 28, 1997
DATE CON	SIDER	D 9/18/06		EXAMINER A MAN ATOMA		
EXAMINER	Initial i	f reference considered	whether or not cital	tion is in conformance with MPEP § 609; draw line	through citation if not	n conformance and

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

No. 3, July 2001, pp. 189-196.  BL Talla et al., "Evaluating Signal Processing and Multimedia Applications on SIMD, VLIW and Super Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.  BM Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.  BN Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19 <sup>th</sup> , Phoenix, Feb. 20-22, 2000, pp. 516-524.  BO IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance	Omit 10-1445				ATTORNEY DOCKET NO.	SERIAL NO.		
APPLICANT   (Use severel sheets if necessery)   APPLICANT   Dimpsey et al.	LICT OF PRIOR ART CITED				AUS920040064US1	10/806,871		
APPLICANT Dimpsey et al.   FILING DATE March 22, 2004   GROUP ART UNIT-2465- 2 ( § 5	L			ITED				
FILING DATE March 22, 2004   GROUP ART UNIT-2465    2/8 5				sary)				
U.S. PATENT DOCUMENTS   CLASS/ SUBCLASS   FILING DATE					APPLICANT Dimpsey et al.			
DOCUMENT NO.   PUBLICATION DATE   DOCUMENT NO.   DATE					FILING DATE March 22, 2004	GROUP ART UNIT	<del>2165</del> 2	185
NO.   DATE   SUBCLASS   DATE   DATE   BB   2002/0129309   Sep. 12, 2002   Floyd et al.   714/724   Dec. 18, 2000   BC   2001/0032305   Oct. 18, 2001   Barry   712/34   Feb. 23, 2001   BD   2002/0199179   Dec. 26, 2002   Lavery et al.   717/158   Jun. 21, 2001   BE   2002/0124237   Sep. 5, 2002   Sprunt et al.   717/127   Dec. 29, 2000   MBF   2002/0147965   Oct. 10, 2002   Swaine et al.   717/124   Feb. 1, 2001   May 25, 2001   Oct. 10, 2002   Swaine et al.   717/137   May 25, 2001   May 25, 2001   May 25, 2001   May 25, 2001   May 26, 2001   May 26, 2001   May 27, 2001   May 27, 2001   May 27, 2001   May 27, 2001   May 28, 2001   May 29, 20				U.	S. PATENT DOCUMENTS			
BB   2002/0129309   Sep. 12, 2002   Floyd et al.   714/724   Dec. 18, 2000	EXAMINER		DOCUMENT	PUBLICATION	INVENTOR NAME			
BC   2001/0032305   Oct. 18, 2001   Barry   T12/34   Feb. 23, 2001	IA							
BD 2002/0199179 Dec. 26, 2002 Lavery et al. 717/158 Jun. 21, 2001  BE 2002/0124237 Sep. 5, 2002 Sprunt et al. 717/127 Dec. 29, 2000  BF 2002/0147965 Oct. 10, 2002 Swaine et al. 717/124 Feb. 1, 2001  BG 2002/019976 Feb. 14, 2002 Patel et al. 717/137 May 25, 2001  FOREIGN PATENT DOCUMENTS  EXAMINER NO. DOCUMENT PUBLICATION DATE SUBCLASS TRANSLATION YES NO DATE SUBCLASS YES NO TRANSLATION YES NO TRANSLATION YES NO THE PRIOR ART (including author, title, date, pertinent page, etc.)  BJ 192000347863 Dec. 15, 2000 Japan G06F 9/38 X  OTHER PRIOR ART (including author, title, date, pertinent page, etc.)  BJ Kikuchi, "Parallelization Assist System", Johe Shori, Vol. 34, No. 9, Sept. 1993, pp. 1158-1169.  BL Talla et al., "Hardware-Assisted Characterization of NAS Benchmarks", Cluster Computing, Vol. 4, No. 3, July 2001, pp. 189-196.  BM Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.  BN Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19 <sup>th</sup> , Phoenix, Feb. 20-22, 2000, pp. 516-524.	N.S.			<u> </u>		<u> </u>		
BE 2002/0124237 Sep. 5, 2002 Sprunt et al. 717/127 Dec. 29, 2000  BF 2002/0147965 Oct. 10, 2002 Swaine et al. 717/124 Feb. 1, 2001  FOREIGN PATENT DOCUMENTS  EXAMINER INITIAL DOCUMENT NO. DATE COUNTRY CLASS/ SUBCLASS YES NO  BJ JP2000029731 Dec. 8, 1999 Japan G06F 9/38 X  OTHER PRIOR ART (including author, title, date, pertinent page, etc.)  BK Kikuchi, "Parallelization Assiet System", Johe Shori, Vol. 24, No. 9, Sept. 1993, pp. 1158-1169.  BK Cohen et al., "Hardware-Assisted Characterization of NAS Benchmarks", Cluster Computing, Vol. 4, No. 3, July 2001, pp. 189-196.  BL Talla et al., "Evaluating Signal Processing and Multimedia Applications on SIMD, VLIW and Super Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.  BM I wasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.  BN Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19th, Phoenix, Feb. 20-22, 2000, pp. 516-524.  BO IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance		BC			<u> </u>	L	·	
BF 2002/0147965 Oct. 10, 2002 Swaine et al. 717/124 Feb. 1, 2001    Foreign Patent Documents   Foreign Patent Publication   Foreign Patent Documents		BD	2002/0199179	Dec. 26, 2002	Lavery et al.			
Foreign Patent Documents    Foreign Patent Document   Publication   Document   Publication   Date   Document   Publication   Date   Document   Publication   Date   Document   Publication   Date   Da		BE	2002/0124237	Sep. 5, 2002	Sprunt et al.	717/127	Dec. 29,	2000
FOREIGN PATENT DOCUMENTS  EXAMINER INITIAL  DOCUMENT DATE  DOCUMENT DATE  DOCUMENT NO.  DATE  DOCUMENT SUBCLASS TRANSLATION SUBCLASS YES NO  A SUBCLASS YES NO  GO6F 9/38  X  OTHER PRIOR ART (including author, title, date, pertinent page, etc.)  BJ Kikuchi. "Parallelization Assiet System", Johe Shori, Vol. 34, No. 9, Sept. 1993, pp. 1158-1169.  BK Cohen et al., "Hardware-Assisted Characterization of NAS Benchmarks", Cluster Computing, Vol. 4, No. 3, July 2001, pp. 189-196.  BL Talla et al., "Evaluating Signal Processing and Multimedia Applications on SIMD, VLIW and Super Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.  BM Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.  BN Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19th, Phoenix, Feb. 20-22, 2000, pp. 516-524.		BF	2002/0147965	Oct. 10, 2002	Swaine et al.	717/124	Feb. 1, 2	2001
EXAMINER INITIAL DOCUMENT NO. DATE COUNTRY CLASS/ SUBCLASS YES NO    NO. DATE DATE SUBCLASS YES NO   NO. DATE SUBCLASS YES NO   N	15	BG	2002/0019976	Feb. 14, 2002	Patel et al.	717/137	May 25,	2001
NO.   DATE   SUBCLASS   YES   NO		•	<u> </u>	FOR	EIGN PATENT DOCUMENTS			-
BH JP200029731 Dec. 8, 1999 Japan G06F 9/38 X  OTHER PRIOR ART (including author, title, date, pertinent page, etc.)  BJ Kikuchi. "Parallelization Assiet System", John Shori, Vol. 34, No. 9, Sept. 1993, pp. 1158-1169.  BK Cohen et al., "Hardware-Assisted Characterization of NAS Benchmarks", Cluster Computing, Vol. 4, No. 3, July 2001, pp. 189-196.  BL Talla et al., "Evaluating Signal Processing and Multimedia Applications on SIMD, VLIW and Super Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.  BM Ivasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.  BN Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19 <sup>th</sup> , Phoenix, Feb. 20-22, 2000, pp. 516-524.	EXAMINER	Ī	DOCUMENT	PUBLICATION	COUNTRY		TRANS	
OTHER PRIOR ART (including author, title, date, pertinent page, etc.)  BJ Kikuchi. "Parallelization Assiet System", Johe Shori, Vol. 34, No. 9, Sept. 1993, pp. 1158-1169.  BK Cohen et al., "Hardware-Assisted Characterization of NAS Benchmarks", Cluster Computing, Vol. 4, No. 3, July 2001, pp. 189-196.  BL Talla et al., "Evaluating Signal Processing and Multimedia Applications on SIMD, VLIW and Super Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.  BM Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.  BN Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19 <sup>th</sup> , Phoenix, Feb. 20-22, 2000, pp. 516-524.	INITIAL	<u> </u>	NO.			<del> </del>	YES	·
OTHER PRIOR ART (including author, title, date, pertinent page, etc.)  BJ Kikuchi. "Parallelization Assist System", Johe Shori, Vol. 34, No. 9, Sept. 1993, pp. 1158-1169.  BK Cohen et al., "Hardware-Assisted Characterization of NAS Benchmarks", Cluster Computing, Vol. 4, No. 3, July 2001, pp. 189-196.  BL Talla et al., "Evaluating Signal Processing and Multimedia Applications on SIMD, VLIW and Super Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.  BM Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.  BN Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19 <sup>th</sup> , Phoenix, Feb. 20-22, 2000, pp. 516-524.	٨١٤	вн	JP2000029731	Dec. 8, 1999	Japan	G06F 9/38		
BJ Kikuchi. "Parallelization Assiet System", Johe Shori, Vol. 34, No. 9, Sept. 1993, pp. 1158-1169.    Solution   Solution   Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.    BM   Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.    BN   Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19th, Phoenix, Feb. 20-22, 2000, pp. 516-524.    Cohen et al., "Hardware-Assisted Characterization of NAS Benchmarks", Cluster Computing, Vol. 4, No. 3, Aug. 1993, pp. 1158-1169.	1.5,	ВІ	JP2000347863	Dec. 15, 2000	Japan	G06F 9/38	<u> </u>	X
BK Cohen et al., "Hardware-Assisted Characterization of NAS Benchmarks", Cluster Computing, Vol. 4, No. 3, July 2001, pp. 189-196.  BL Talla et al., "Evaluating Signal Processing and Multimedia Applications on SIMD, VLIW and Super Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.  BM Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.  BN Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19 <sup>th</sup> , Phoenix, Feb. 20-22, 2000, pp. 516-524.  BO IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance								
No. 3, July 2001, pp. 189-196.  BL Talla et al., "Evaluating Signal Processing and Multimedia Applications on SIMD, VLIW and Super Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.  BM Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.  BN Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19 <sup>th</sup> , Phoenix, Feb. 20-22, 2000, pp. 516-524.  BO IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance					-			
Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.  BM Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.  BN Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19 <sup>th</sup> , Phoenix, Feb. 20-22, 2000, pp. 516-524.  BO IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance	1.5,	ВК	No. 3. July 2001	. pp. 189-196.				
163-172.   BM   Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.   BN   Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19 <sup>th</sup> , Phoenix, Feb. 20-22, 2000, pp. 516-524.   BO   IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance		BL	Talla et al., "Eva	luating Signal	Processing and Multimedia Application	ns on SIMD, VL	W and S	uper
BM Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.  BN Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19 <sup>th</sup> , Phoenix, Feb. 20-22, 2000, pp. 516-524.  BO IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance				ures", Internat	ional Conference on Computer Design,	, Austin, Sept. 1	7-20, 20	uu, pp.
BN Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19 <sup>th</sup> , Phoenix, Feb. 20-22, 2000, pp. 516-524.  BO IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance		ВМ	lwasawa et al., "	<b>Parallelization</b>	Method of Fortran DO Loops by Paralle	elizing Assist S	ystem",	
International Performance, Computing, and Communications Conference, 19 <sup>th</sup> , Phoenix, Feb. 20-22, 2000, pp. 516-524.  BO IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance		DAI	Transactions of	Information Pr	ocessings Society of Japan, Vol. 36, Notice of Multimodia Applications on	o. 8, Aug. 1995,	pp. 199:	5-2006.
2000, pp. 516-524.     BO   IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance		DN	International Pe	rformance, Co	mputing, and Communications Confere	ence, 19 <sup>th</sup> , Phoe	nix, Feb.	20-22,
📝 🕻  BU  IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance	V		2000, pp. 516-52	<b>4</b> .				
Measurement Tool for Software Development Cost Reduction", April 2001, pp. 686-688.	1.5.	BO	IIBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance Measurement Tool for Software Development Cost Reduction" April 2001, pp. 686-688.					
RELATED PATENT APPLICATIONS								
EXAMINER APPLICATION NO./ APPLICANT TITLE FILING	EXAMINER	П	APPLICATION NO./	APPLICANT	TITLE		1	
INITIAL ATTY. DOCKET NO. DATE	INITIAL		<u> </u>	L				
Davidson et al. Method and Apparatus for Instruction Sampling for Nov. 4, 1999  AT9-99-491  Davidson et al. Method and Apparatus for Instruction Sampling for Nov. 4, 1999	· A.S.	ВР		Davidson et al.			Nov. 4,	1999
BQ 08/538,071 Gover et al. Method and System for Selecting and Distinguishing Oct. 2, 1995	1 1	BQ	08/538,071	Gover et al.	Method and System for Selecting and		Oct. 2, 1	1995
AUT919950055US1 an Event Sequence using an Effective Address in a Processing System	1 11.5		AUT919950055US1			Address in a		
DATE CONSIDERED 9/18/06 EXAMINER Somm Hand	DATE CONS	IDERE	D 9/18/11	<u> </u>	EXAMINER / Harb		J	
EXAMINER: Initial if reference considered, whether or not citation is in conformance with PEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	EXAMINER:	Initial i	reference considered,	whether or not cita	tion is in conformance with MPEP § 609; draw line the	rough citation if not in	n conformat	nce and

Form PTO-1449				ATTORNEY DOCKET NO.	SERIAL NO.	
=				AUS920040064US1	10/806,871	
L		OF PRIOR ART C	ITED			
		BY APPLICANT several sheets if necess	sary)			
	, 550 0			APPLICANT Dimpose et al	L	
				APPLICANT Dimpsey et al.		-
				FILING DATE March 22, 2004	GROUP ART UNIT	<del>2185</del> 2185
			RELA	TIED PATIENT APPLICATIONS	L	
EXAMINER		APPLICATION NO./	APPLICANT	TITLE		FILING
INITIAL		ATTY. DOCKET NO.				DATE
A .	BR	10/675,777 /	DeWitt, Jr. et al.	Method and Apparatus for Counting In	struction	Sep. 30, 2003
A.S.		AUS920030477US1		Execution and Data Accesses		
Ì	BS	10/674,604 /	DeWitt, Jr. et al.	Method and Apparatus for Selectively	Counting	Sep. 30, 2003
		AUS920030478US1	DeWitt, Jr. et al.	Instructions and Data Accesses  Method and Apparatus for Generating	Intersucts	Sep. 30, 2003
	вт	10/675,831 / AUS920030479US1	Davill, Jr. et al.	Method and Apparatus for Generating   Upon Execution of Marked Instruction:		3ep. 30, 2003
				Access to Marked Memory Locations	o una opon	
	BU	10/675,778 /	DeWitt, Jr. et al.	Method and Apparatus for Counting D		Sep. 30, 2003
		AUS920030480US1		and Instruction Executions that Excee		
	BV	10/675,776 /	DeWitt, Jr. et al.	Method and Apparatus for Counting E		Sep. 30, 2003
		AUS920030481US1		Specific Instructions and Accesses to Locations	Specific Data	
	RW	10/675,751 /	DeWitt, Jr. et al.	Method and Apparatus for Debug Sup	port for	Sep. 30, 2003
	511	AUS920030482US1		Individual Instructions and Memory Lo		- Sp. 55, 2000
	вх	10/675,721 /	Levine et al.	Method and Apparatus to Autonomica		Sep. 30, 2003
		AUS920030483US1		Instructions for Selective Counting		
	BY	10/674,642 / AUS920030484US1	Levine et al.	Method and Apparatus to Autonomica	lly Count	Sep. 30, 2003
	D7		Levine et al.	Instruction Execution for Applications Method and Apparatus to Autonomica	lly Take an	Sep. 30, 2003
	BZ	10/674,606 / AUS920030485US1	Lavilla at al.	Execution on Specified Instructions	runo un	
	CA	10/675,783 /	Levine et al.	Method and Apparatus to Autonomica	lly Profile	Sep. 30, 2003
		AUS920030486US1		Applications		
	СВ	10/675,872 /	DeWitt, Jr. et al.	Method and Apparatus for Counting Ir	struction and	Sep. 30, 2003
	-	AUS920030487US1	Levine et el	Memory Location Ranges Method and Apparatus for Maintaining	Dorformanco	Jan. 14, 2004
	CC	10/757,250 / AUS920030488US1	Levine et al.	Monitoring Structures in a Page Table		Jan. 14, 2004
-				Monitoring Performance of a Compute	r Program	1
1/	CD	10/757,192 /	DeWitt, Jr. et al.	Method and Apparatus for Providing F	Pre and Post	Jan. 14, 2004
lacksquare		AUS920030543US1		Handlers for Recording Events		
آ م ا	CE	10/757,192 /	DeWitt, Jr. et al.	Method and Apparatus for Determining		March 18, 2004
1/1 4	ĺ	AUS920030548US1		Program Flows Autonomically Using I Assisted Thread Stack Tracking and C		
ハル				Symbolic Data	ratarogea	
DATE CONS	IDERF	D 9/18/66	,	EXAMINER /2 m Alask		
		reference considered	whether or not cita	tion is in conformance with MPEP § 609; draw line th	rough citation if not it	n conformance and

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form PTO-1449				ATTORNEY DOCKET NO.	SERIAL NO.	
	IST (	OF PRIOR ART C	ITED	AUS920040064US1	10/806,871	
BY APPLICANT (Use several sheets if necessary)						
				APPLICANT Dimpsey et al.		
				FILING DATE March 22, 2004	GROUP ART UNIT	~2165-2185
			RELA	EKIOTRASILIARA TKETRARI DETK		
EXAMINER INITIAL		APPLICATION NO./ ATTY, DOCKET NO.	APPLICANT	TITLE		FILING DATE
AS.	CF	10/757,227 / AUS920030555US1	DeWitt, Jr. et al.	Method and Apparatus for Autonomic Cache Entries to Dedicated Storage v Cache Line Sharing is Detected		Jan. 14, 2004
	CG	10/757,197 / AUS920030556US1	DeWitt, Jr. et al.	Method and Apparatus for Optimizing Execution Using Annotated Trace Info having Performance Indicator and Co Information	ormation	Jan. 14, 2004
	СН	10/807,094 / AUS920040059US1	Dimpsey et al.	Method and Apparatus for Providing H Assistance for Code Coverage	ardware	Mar. 22, 2004
	CI	10/808,716 / AUS920040060US1	Dimpsey et al.	Method and Apparatus for Autonomic Feedback Using Hardware Assistance Coverage		Mar. 22, 2004
	CJ	10/806,576 / AUS920040061US1	Dimpsey et al.	Method and Apparatus for Hardware Data Access Coverage	Assistance for	Mar. 22, 2004
	СК	10/806,633 / AUS920040062US1	Dimpsey et al.			Mar. 22, 2004
V	CL	10/806,917 / AUS920040063US1	Dimpsey et al.	Method and Apparatus for Autonomic Test Case Feedback Using Hardware Assistance for Data Coverage  Mar. 22, 200		Mar. 22, 2004
A.S.	CM	10/806,866 / AUS920040065US1	Dimpsey et al.	Method and Apparatus for Prefetchin Data Structure	g Data from a	Mar. 22, 2004
DATE CONSIDERED 9/19/16				EXAMINER My And		
EXAMINER: Initial if reference considered, whether or not citation not considered. Include copy of this form with next communication				tion is in conformance with MPEP § 809, draw line to	hrough citation if not i	n conformance and

PTO/SB/(08-03)

Approved for use through 07/31/2006. OMB 0651-0031

U.S. Patent and Trademark Office. U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number. Substitute for form 1449/PTO Complete if Known

3/	Application Number	10/806,871
MAR 27 2006 8	Filing Date 03-22-2004  PISCLOSURE First Named Inventor Dimpsey et al.	03-22-2004
INITADAMATIAN DICCI ACIDE	First Named Inventor	Dimpsey et al.
STATEMENT BY APPLICANT	Examiner Name	-Popovici, Dov Arogn Saul
THE PENELLINE OF APPLICANT	Art Unit	2105 2185
	Attorney Docket No.	AUS920040064US1

Exam		Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
15	AA	5,710,881	01-20-1998	Gupta et al.	
1	AB	5,822,763	10-13-1998	Baylor et al.	
	AC	5,928,334	07-27-1999	Mandyam et al.	
+	AD	6,094,709	07-25-2000	Baylor et al.	
1	AE	6,285,974 B1	09-04-2001	Mandyam et al.	
4	AF	6,636,950 B1	10-21-2003	Mithal et al.	
A 5.	AG	2004/0205302 A1	10-14-2004	Cantrill	
		·			

ر. جائلة بين	FOREIGN PATENT DOCUMENTS					
Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		

in Fair	NON PATIENT LITTERATURE				
Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				
A,S, AH	TANENBAUM, "Structured Computer Organization", 1984, Prentice-Hall, Inc., 2 <sup>nd</sup> Edition, pp. 10-12.				
A.S. AI	TORRELLAS et al., "False Sharing and Spatial Locality in Multiprocessor Caches", June 1994, IEEE Transactions on Computers, Vol. 43, No. 6, pp. 651-663.				
A.S. AJ	ROTHMAN et al., "Analysis of Shared Memory Misses and Reference Patterns", 2000, IEEE, pp. 187-198.				

Examiner:

Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

PTO/SB/(08-03)
Approved for use through 07/31/2006. OMB 0651-0031
U.S. Patent and Trademark Office. U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number. Substitute for form 1449/PTO

FARE INFORMATION DISCLOSURE STATEMENT BY APPLICANT

	Complete if Known				
Application Number	10/806,871				
Filing Date	03-22-2004				
First Named Inventor	Dimpsey et al.				
Examiner Name	Popovici, Dov Arpan Savla				
Art Unit	2165 2185				
Attorney Docket No.	AUS920040064US1				

Exam Initia		Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
A D	AA	5,797,019	08-18-1998	Levine et al.	
	AB	6,549,998 B1	04-15-2003	Pekarich et al.	
	AC	2003/0101367 A1	05-29-2003	Bartfai et ai.	
$\neg \neg$	AD	5,113,507	05-12-1992	Jaeckel	
V	AE	6,681,387 B1	01-20-2004	Hwu et al.	
7.1.5	AF	6,925,424 B2	08-02-2005	Jones et al.	
			· · ·	<u>'</u>	
				·	

	111		FOREIGN PATENT	DOGUMĒNIS (1155) M.S.	
Examli Initia		Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

44.1		A STATE OF THE STA			
Exam	iner	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item			
Initial		(book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s),			
		publisher, city and/or country where published.			
110	AG	Wikipedia "JavaServer Pages" downloaded 1/24/06			
[NS]		http://en.wikipedia.org/wiki/JavaServer_Pages			
A.3.	AH	"Hardware Cycle Based memory Residency", IBM, May 22, 2003, ip.com, IPCOM000012728D, pp. 1-2			
AS	Al	HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from <a href="http://webster.cs.ucr.edu/AoA/Linux/PDFs/0">http://webster.cs.ucr.edu/AoA/Linux/PDFs/0</a> PDFindexLinux.html			
1,17					
$\vdash$					
1					

Examiner:

Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

PTO/SB/(08-03)
Approved for use through 07/31/2006. OMB 0851-0031
U.S. Patent and Trademark Office. U.S. DEPARTMENT OF COMMERCE
Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

Substitute for form 1449/PT¢

MAY R 1 2006

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

ō١	Complete if Known					
'À	plication Number	10/806,871				
Æ	ing Date	03-22-2004				
ÿi	rst Named Inventor	Dimpsey et al.				
E	xaminer Name	Popovici, Dov				
Ar	rt Unit	2165				
At	ttorney Docket No.	AUS920040064US1				

Examiner Initials			Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
1/1	1151		5394529	02-28-1995	Brown, III et al.	
ĺ		AB	6233679 B1	05-15-2001	Holmberg	
		AC	5887159	03-23-1999	Burrows	
		AD	6662295 B2	12-09-2003	Yamaura	
		AE	5938760	08-17-1999	Levine et al.	
		AF	6189072 B1	02-13-2001	Levine et al.	
		AG	5276833	01-04-1994	Auvinen et al.	
		AH	5287481	02-15-1994	Lin	
		Al	5537572	07-16-1996	Michelsen et al.	
		AJ	5805879	09-08-1998	Hervin et al.	
		AK	6647301 B2	11-11-2003	Sederlund et al.	
		AL	6735666 B1	05-11-2004	Koning	
	,	AM	2004/0153612 A1	08-05-2004	Mutz et al.	
7	$\mathcal{T}$	AN	2005/0091456 A1	04-28-2005	Huck	
7	5	AO	2005/0108483 A1	05-19-2005	Bungo	

MANAGE BY	racametrical and the American	FOREIGN PATEN	T DOGUMENTS ,	19 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	

Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s),					
A.S. AP	publisher, city and/or country where published.  JEONG et al., "Cost Sensitive Cache Replacement Algorithms", June 2002, Second Workshop on Cashing, Coherence and Consistency, NY, pp. 1-14.					

Examiner:

**Date Considered:** 

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

	Application Number	10806871		
	Filing Date	2004-03-22		
INFORMATION DISCLOSURE	First Named Inventor Dim	psey et al.		
STATEMENT BY APPLICANT ( Not for submission under 37 CFR 1.99)	Art Unit	<del>2165</del> 2185		
(Not for Submission under or of it 1.55)	Examiner Name Sam	in, Jemey A. Arpan Savla		
	Attorney Docket Number	AUS920040064US1		

	U.S.PATENTS Remove									
Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue D	)ate	Name of Pate of cited Docu	entee or Applicant ment	Pages, Columns, Lines where Relevant Passages or Relev Figures Appear		
A.S.	1	6973417	B1	2005-12	2-06	Maxwell et al.				
AS,	2	6721875	B1	2004-04	4-13	McCormick et	al.			
If you wis	If you wish to add additional U.S. Patent citation information please click					ease click the	Add button.		Add	
U.S.PATENT APPLICATION PUBLICATIONS Remove										
Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date		Name of Patentee or Applicant of cited Document		Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear		
	1									
If you wis	h to ac	dd additional U.S. Publis	shed Ap	plication	citation	information p	lease click the Add	d button	Add	
				FOREIG	SN PAT	ENT DOCUM	ENTS		Remove	
Examiner Initial*	Cite No	Foreign Decument Number <sup>3</sup>	Country Code <sup>2</sup>	•	Kind Code <sup>4</sup>	Publication Date	Name of Patented Applicant of cited Document	e or V	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear	T5
	1									
If you wis	h to ac	dd additional Foreign Pa	tent Do	cument	citation	information pl	ease click the Add	button	Add	
			NON	I-PATE	NT LITE	RATURE DO	CUMENTS		Remove	

## **Application Number** 10806871 Filing Date 2004-03-22 INFORMATION DISCLOSURE First Named Inventor Dimpsey et al. STATEMENT BY APPLICANT Art Unit 2165 2185 ( Not for submission under 37 CFR 1.99) **Examiner Name** -Gaffin, Jeffrey A. Savla Attorney Docket Number AUS920040064US1

Examiner Initials*	Cite No	. Tipook madazine loomal sehal symbosium Caraloo elc) dale dadesis), volume-issue humberts).					
	1						
If you wis	h to a	dd additional non-patent literature document citation information please click the Add button Add	1				
		EXAMINER SIGNATURE					
Examiner	Signa	ature Amb Date Considered 9/18/86					
		itial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a conformance and not considered. Include copy of this form with next communication to applicant.					
Standard S	r.3). <sup>3</sup> f cument	of USPTO Patent Documents at <a href="https://www.USPTO.GOV">www.USPTO.GOV</a> or MPEP 901.04. <sup>2</sup> Enter office that issued the document, by the two-letter code (W For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>5</sup> Applicant is to place a check mark anslation is attached.	cument.				